

Notice of References Cited		Application/Control No. 10/811,222	Applicant(s)/Patent Under Reexamination CHAO ET AL.	
		Examiner TIPHANY B. DICKERSON	Art Unit 3623	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2005/0038684	02-2005	Wang et al.	705/008
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Çatay, "Tool Capacity Planning in Semiconductor Manufacturing," August 2003, Computers & Operations Research, Vol. 30, No. 9, pp. 1349-1366.
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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